

Figure 1

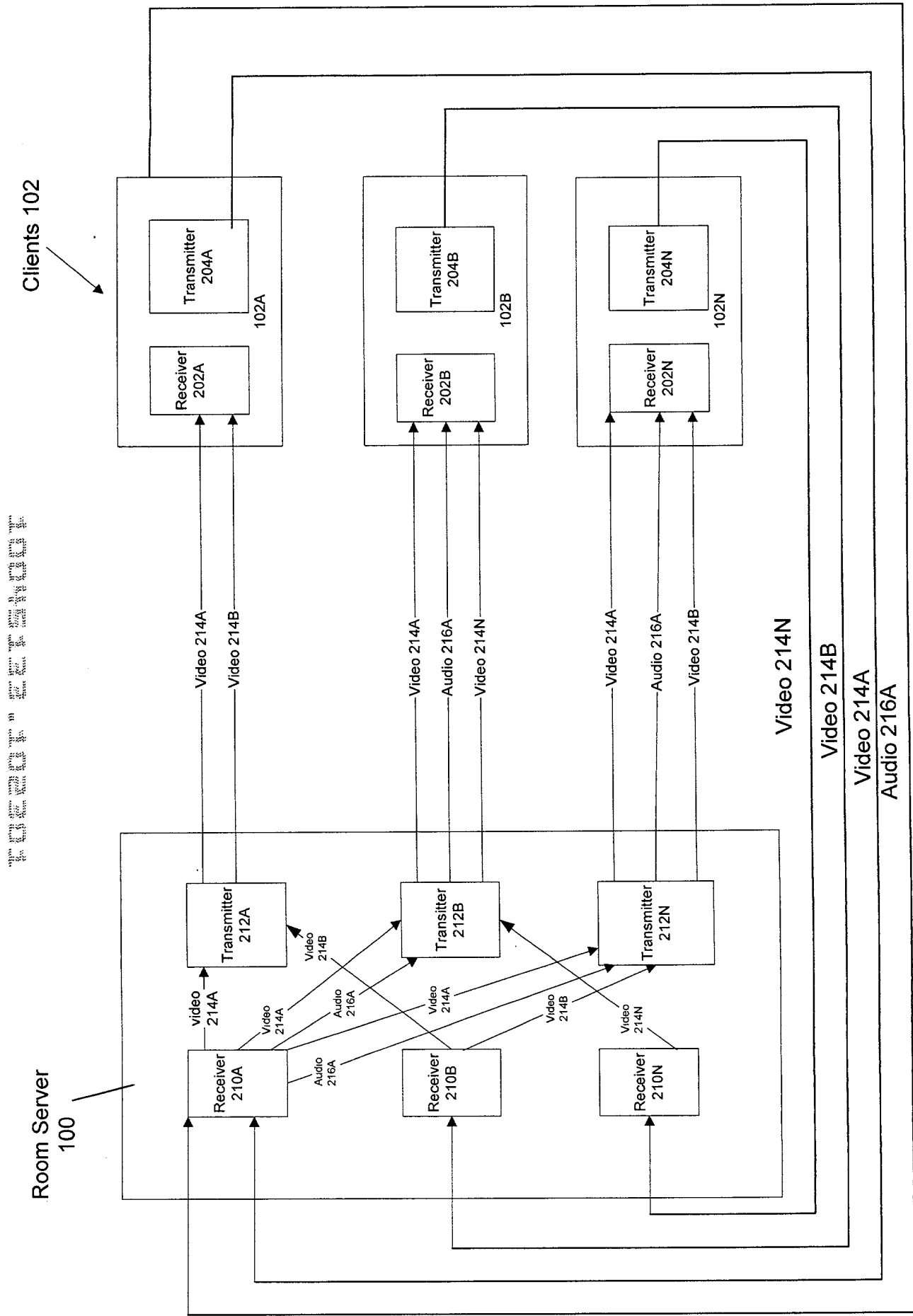


Figure 2

FIG. 3 is a block diagram of a room server 110, in accordance with an embodiment of the present invention. The room server 110 includes a receiver 210 and a transmitter 212. The receiver 210 includes a sequencer 306. The transmitter 212 includes an audio resampler 308, a video resampler 310, a video mux 314, and an MHAQ 312. The room server 110 also includes an AMB 316. The room server 110 is connected to a client 102. The client 102 sends data to the room server 110 via a connection labeled "From client 102". The room server 110 sends data to the client 102 via a connection labeled "To client 102".

Room Server
110

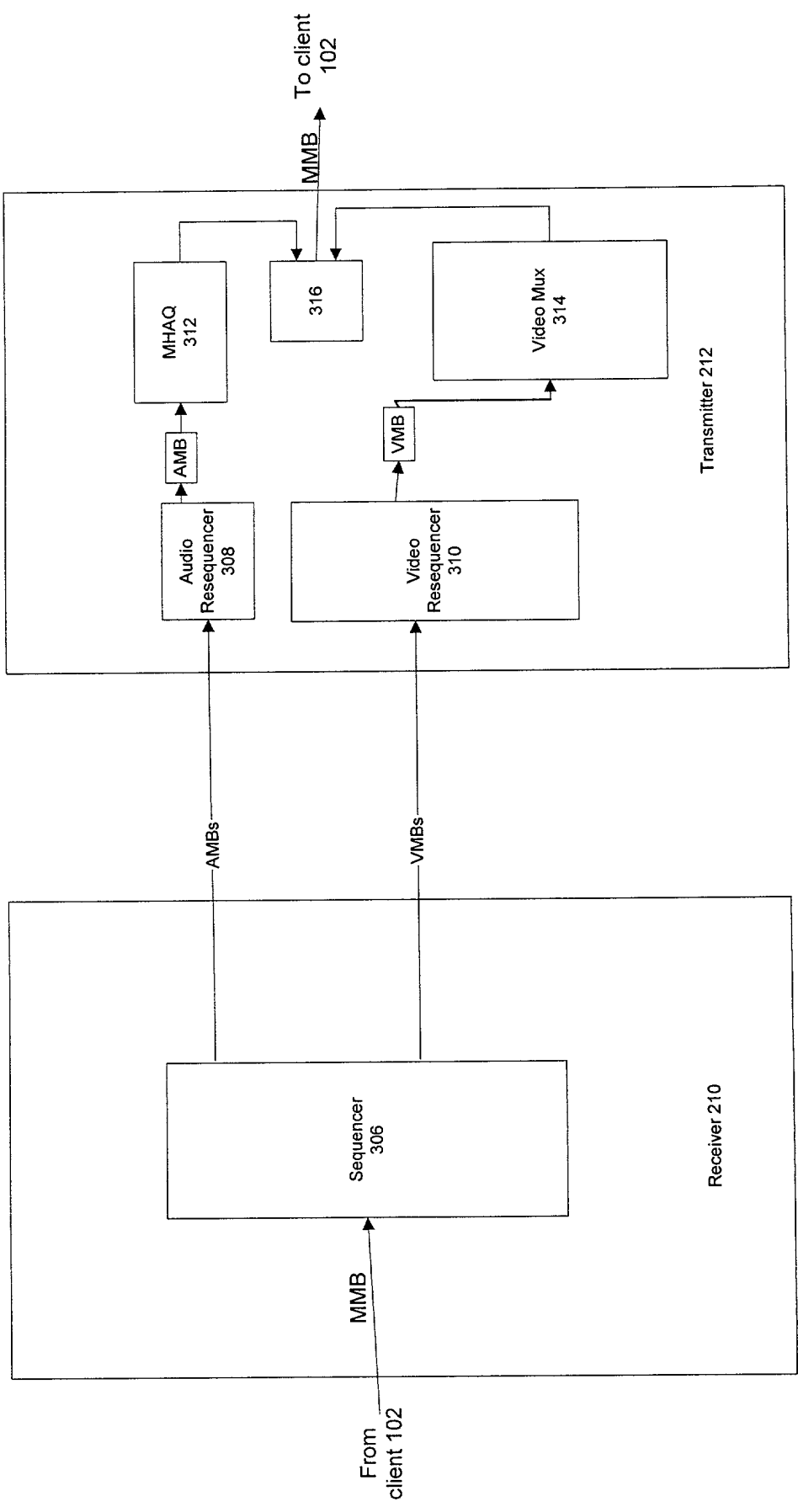


Figure 3

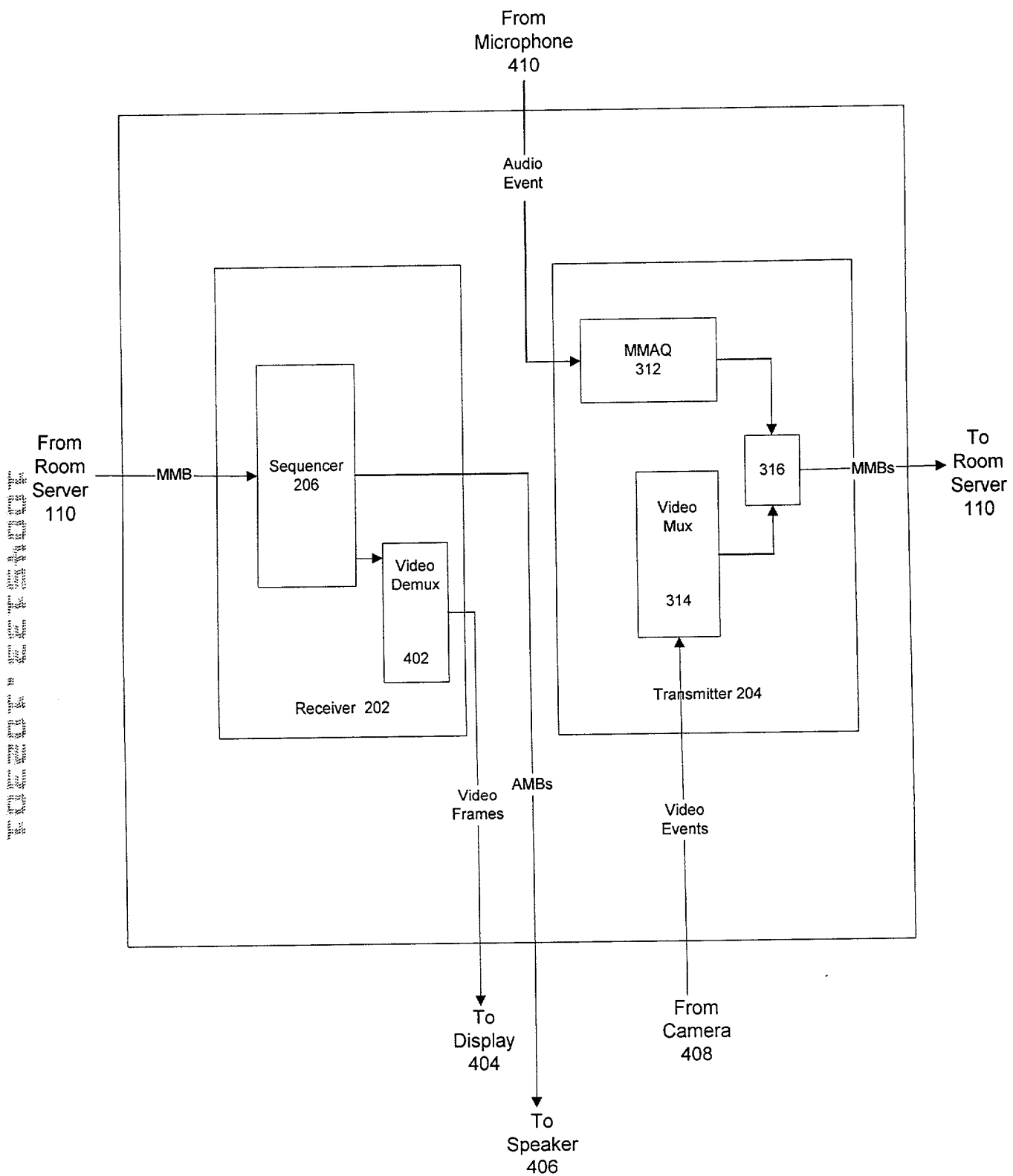


Figure 4

Request Processor
500

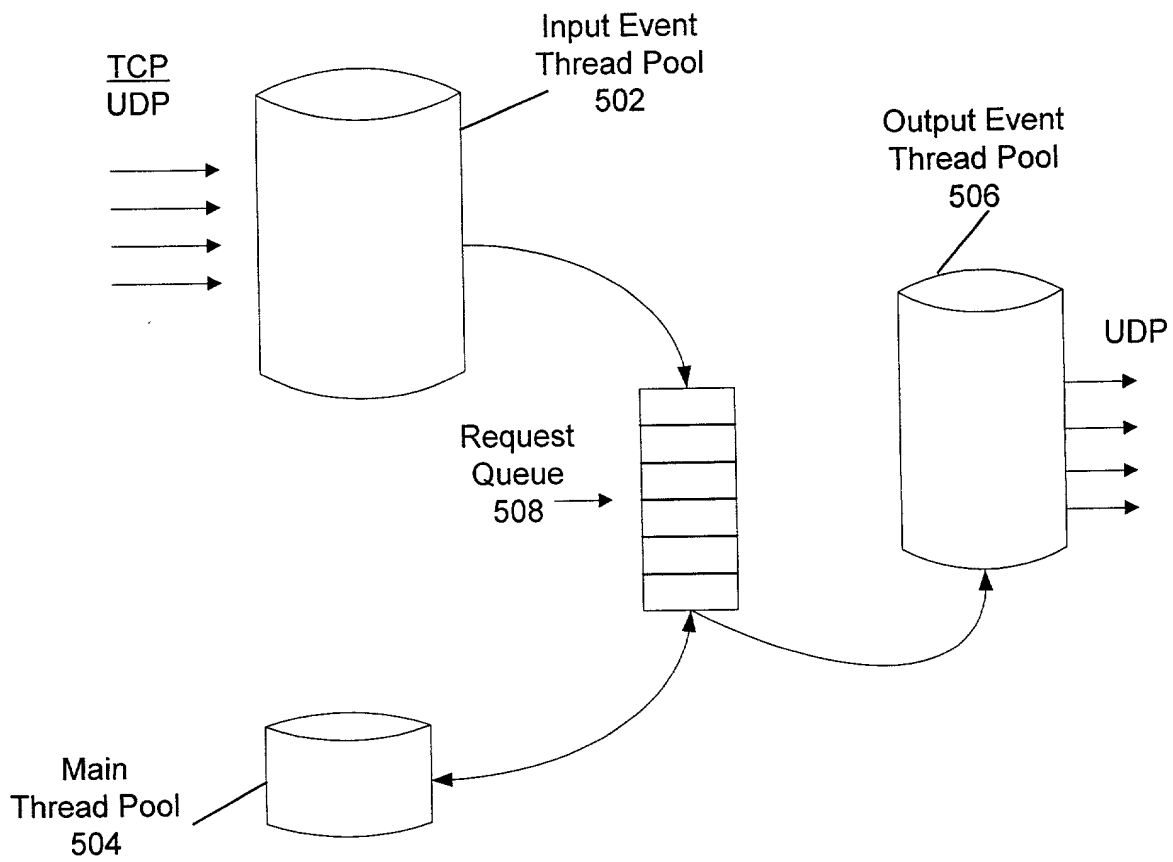


Figure 5

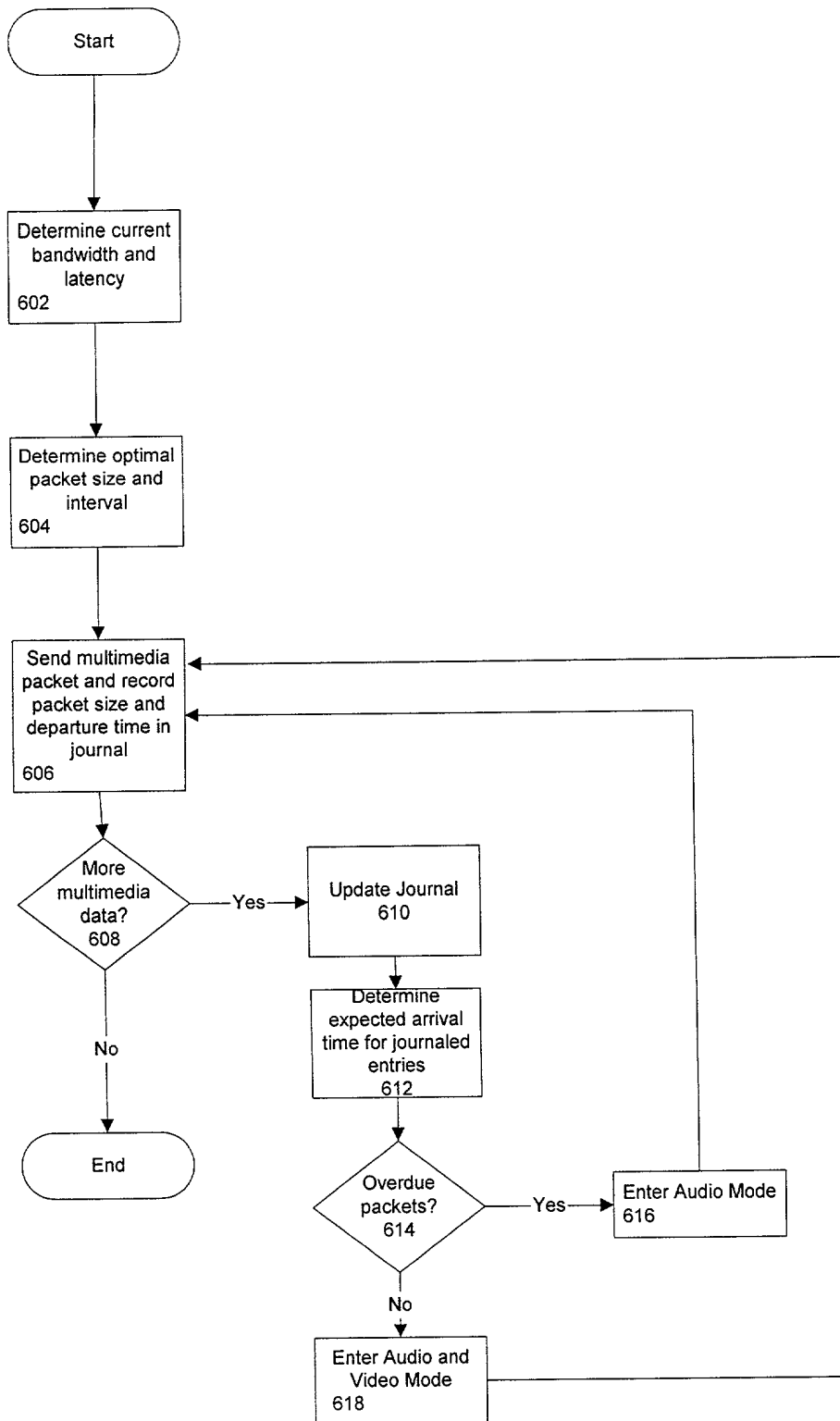


Figure 6

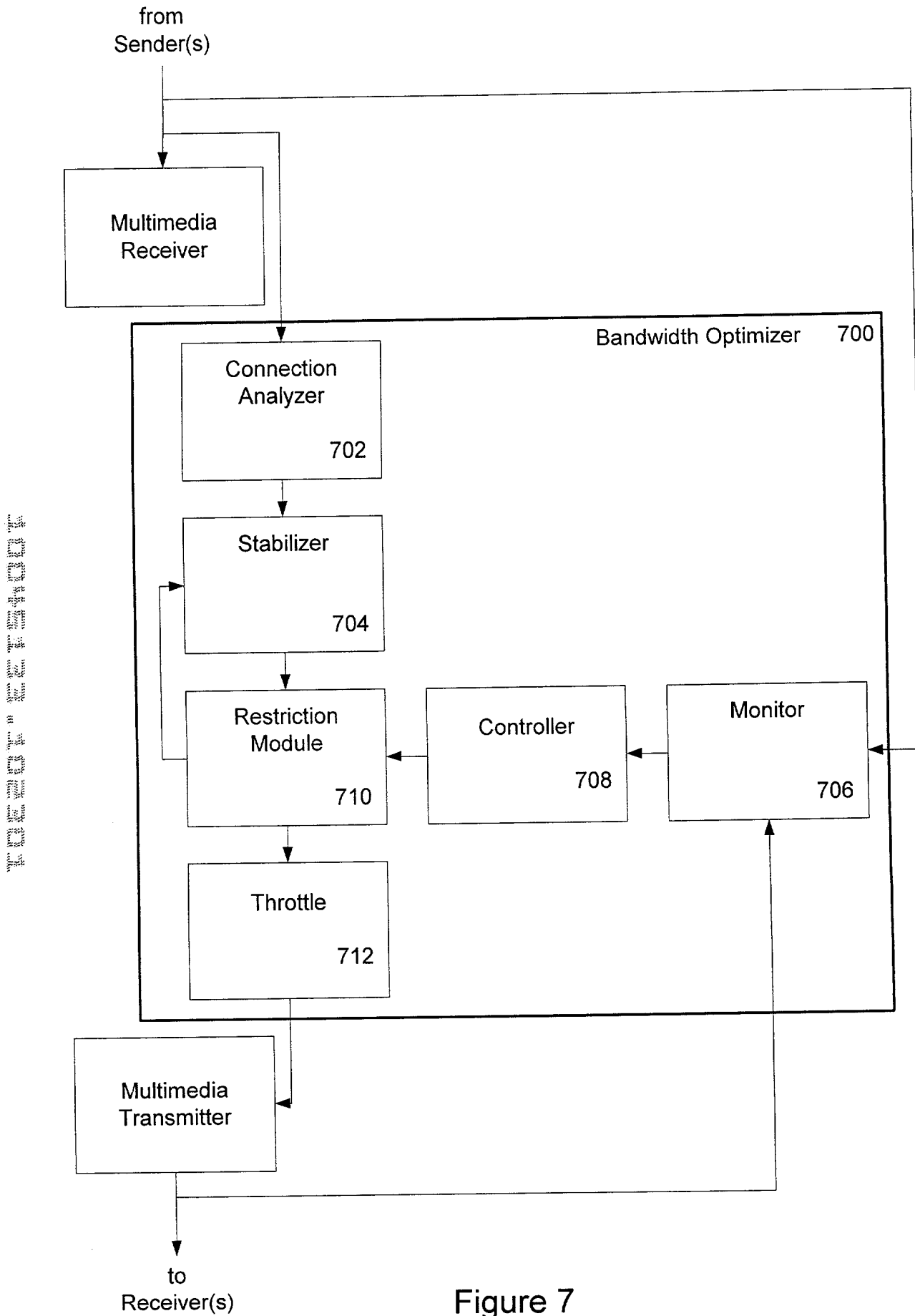


Figure 7

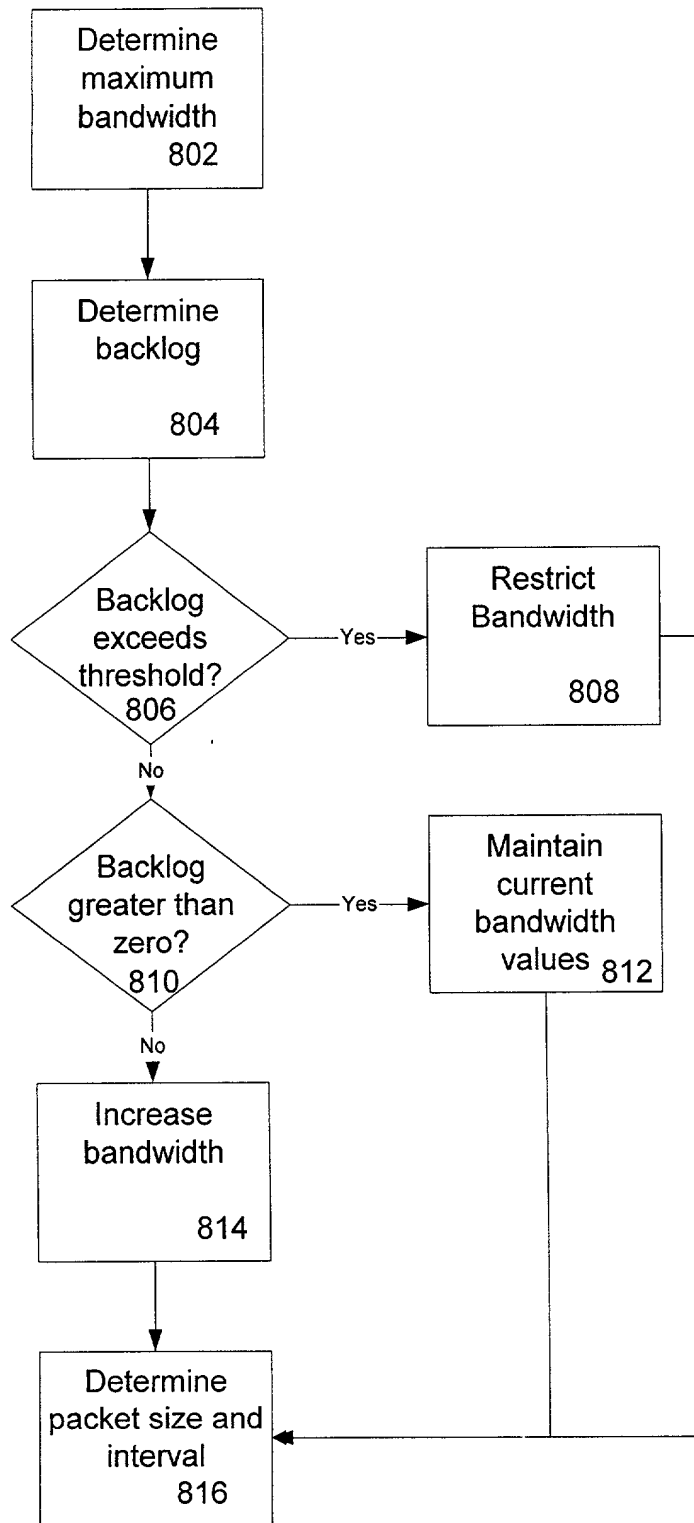


Figure 8

Latency Timeline

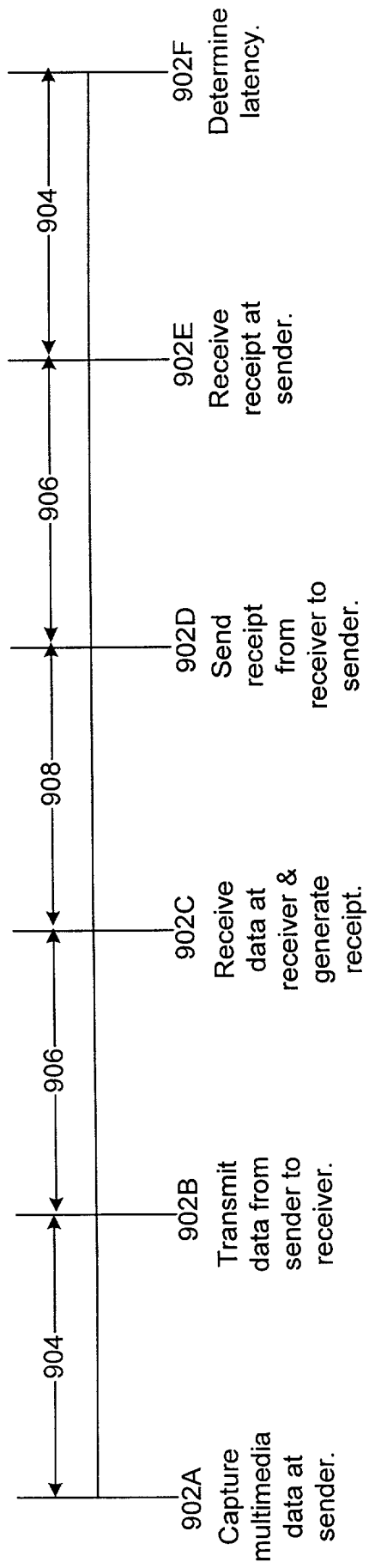


Figure 9

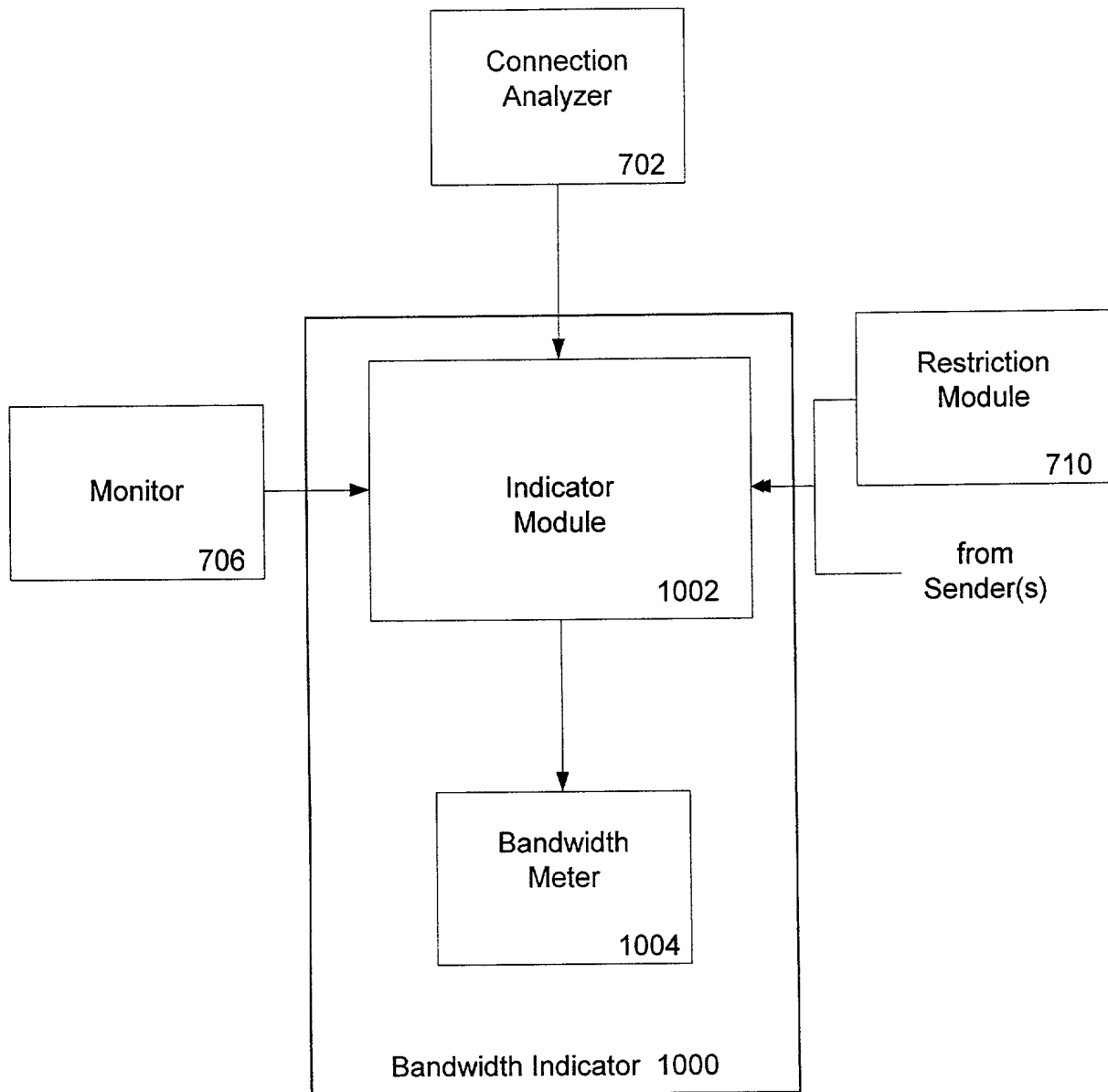


Figure 10

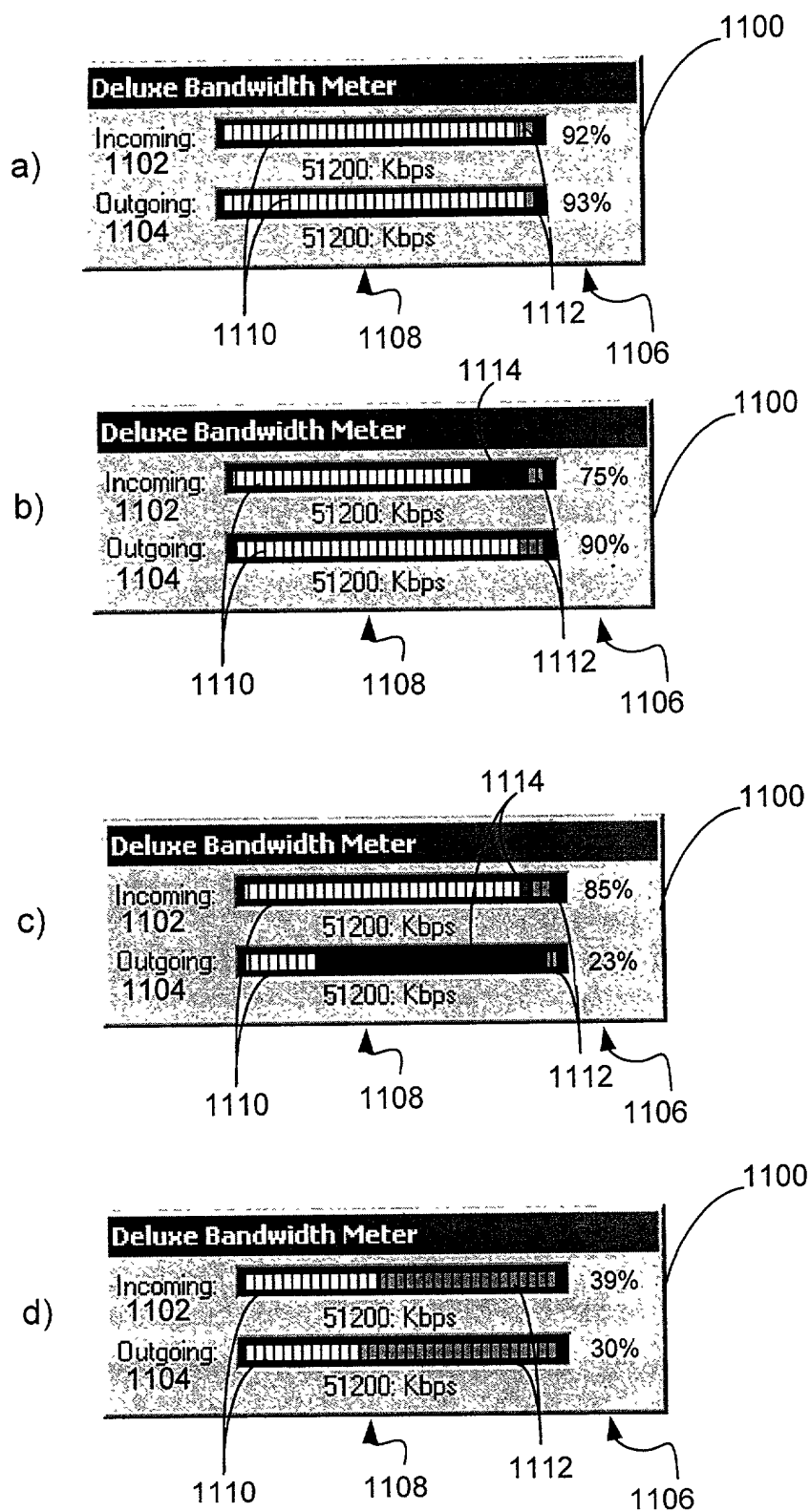


Figure 11

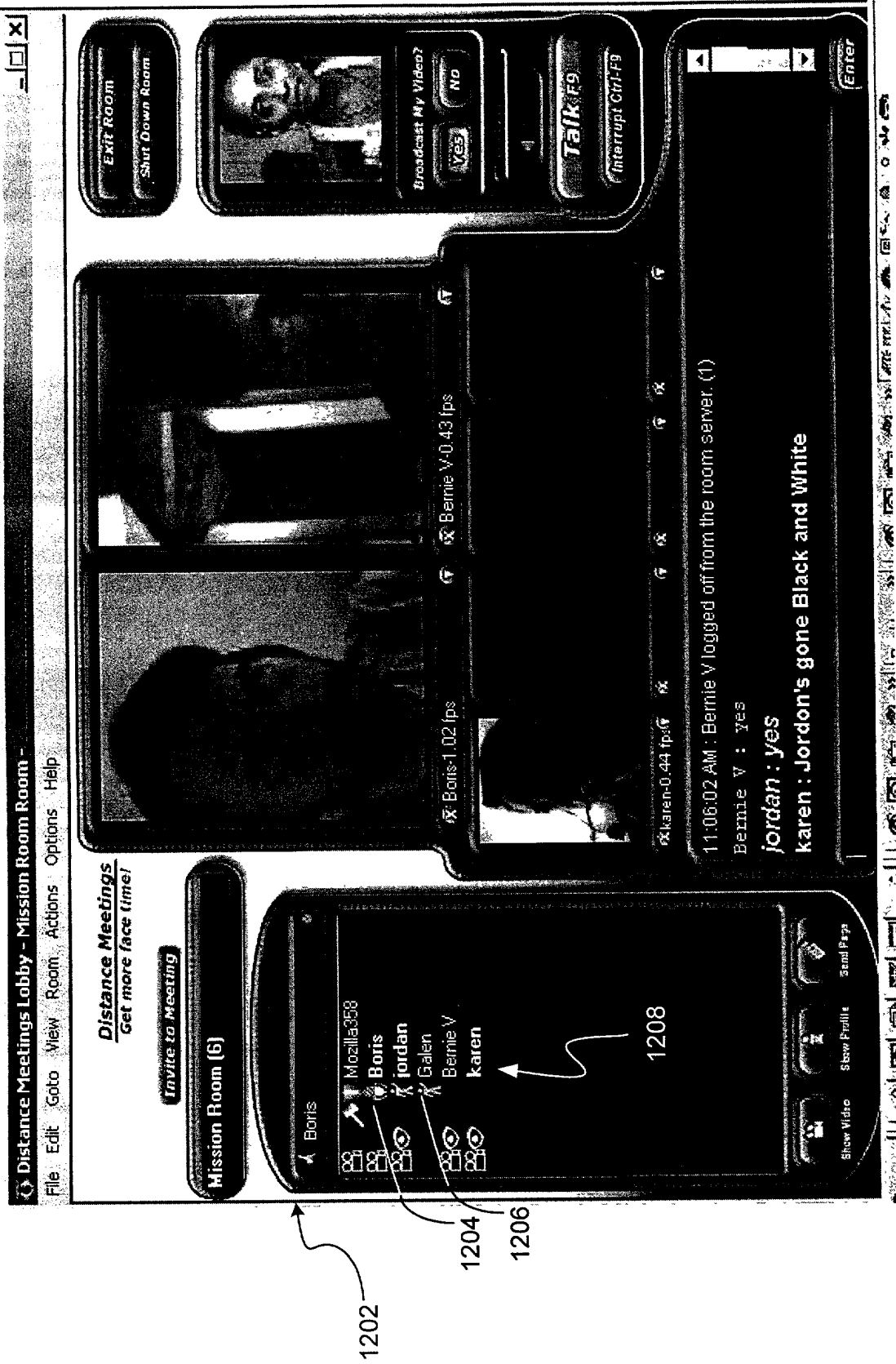


Figure 12

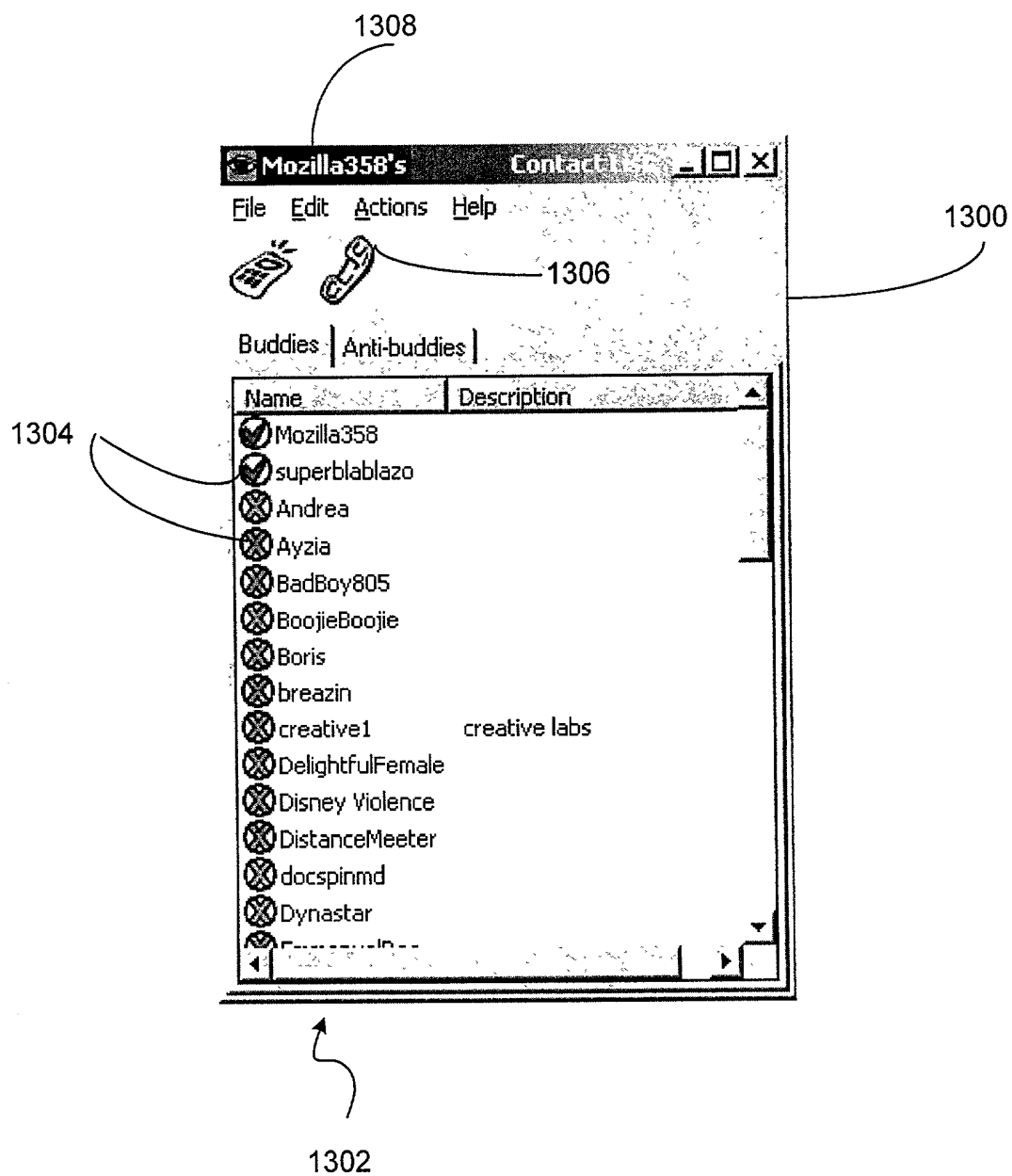


Figure 13